

10/033,337

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,781,892 B2  
DATED : August 24, 2004  
INVENTOR(S) : Hsu et al.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4,

Line 6, delete "MOSFET" and insert -- nMOSFET --.

Column 8,

Line 9, delete "in" and insert -- an --.

Column 9,

Line 36, delete " $2^{n-m}$ " and insert --  $2^m$  --.

Column 10,

Line 40, delete " $2^{m+k}$  foot" and insert --  $2^{m+k}$  foot --.

Signed and Sealed this

Third Day of May, 2005



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*